LIBRARY NUMERIC\_STD, PART I

# Objectives

* To become familiar with the implementation of the *numeric\_std* library in a VHDL system, by developing a simple signed calculator

# Lab work

Design in VHDL a signed calculator with the following inputs and outputs:

* Inputs
  + Clock
  + Number A
  + Number B
  + Operation selector
  + Display selector
* Outputs
  + Sign bit
  + Operation LEDs
  + 7-segment display data
  + 7-segment display enable

# Report

Turn in a technical report including the following:

* Links to GitHub of Activities of part I and II
* Links to YouTube with demo videos of your working code
* Screenshots of the Terminal showing the output of your programs
* Conclusions